

TAO-V-SGD2

TAO-V Option Switch Settings: 1(Pulse/ICT104V/ICT104U/MDB)

Supported bill S\$ 2, 5

2bills.

TAO-V dip-switch settings and functions:

| FUNCTION | SW1 | SW2 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 | SW1 | SW2 | SW3 | SW4 |
|---------------------------------|-------------------------------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| ★ Reject S\$ 2 | ON | | | | | | | | | | | |
| ★ Accept S\$ 2 | OFF | | | | | | | | | | | |
| ★ Reject S\$ 5 | | ON | | | | | | | | | | |
| ★ Accept S\$ 5 | | OFF | | | | | | | | | | |
| ★ Reserved | | | ON | | | | | | | | | |
| ★ Reserved | | | OFF | | | | | | | | | |
| ★ Reserved | | | | ON | | | | | | | | |
| ★ Reserved | | | | OFF | | | | | | | | |
| ★ Reserved | | | | | ON | | | | | | | |
| ★ Reserved | | | | | OFF | | | | | | | |
| ★ Stack Banknote when Power-up | | | | | | ON | | | | | | |
| ★ Reject Banknote when Power-up | | | | | | OFF | | | | | | |
| Pulse Mode | Other Mode | | | | | | | | | | | |
| ★ Harness Disable | BA stops working when fishing | | | | | ON | | | | | | |
| ★ Harness Enable | Normal | | | | | OFF | | | | | | |
| ★ Inhibit Active High | | | | | | | ON | | | | | |
| ★ Inhibit Active Low | | | | | | | OFF | | | | | |
| ★ 1 pulse / S\$ 1 | | | | | | | | OFF | OFF | | | |
| ★ 2 pulses / S\$ 1 | | | | | | | | ON | OFF | | | |
| ★ 4 pulses / S\$ 1 | | | | | | | | OFF | ON | | | |
| ★ 20 pulses / S\$ 1 | | | | | | | | ON | ON | | | |
| ★ Interface Timing Conversion | 50ms LO / 50ms HI | | | | | | | | | OFF | OFF | |
| | 60ms LO / 300ms HI | | | | | | | | | ON | OFF | |
| | 30ms LO / 50ms HI | | | | | | | | | OFF | ON | |
| | 150ms LO / 150ms HI | | | | | | | | | ON | ON | |

★ Manufacture setting

◆ Note : (1) Default setting

(2) Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.

(3) Remove the front cover and the dip-switch in on the CPU board.

(Please refer to "SW1" setting.)

TAO-V-SGD2(Pulse/ICT104V/ICT104U/MDB)

Interface Settings: 2(Pulse)

| INTERFACE | SW1 | SW2 | SW3 | SW4 |
|----------------------------|-----|-----|-----|-----|
| ★ Credit-Pulse Normal HIGH | ON | | | |
| Credit-Pulse Normal LOW | OFF | | | |
| ★ Pulse Mode | | ON | OFF | |
| ICT104V Interface | | OFF | ON | |
| ICT104U Interface | | OFF | OFF | |
| MDB Interface | | ON | ON | |
| ★ Reserved | | | | ON |
| | | | | OFF |

Currency Assign Data

| Interface Bill value | MDB | Pulse | ICT104V | ICT104U |
|-------------------------|-------|-------|---------|---------|
| BV1 | SGD 2 | SGD 2 | SGD 2 | SGD 2 |
| BV2 | SGD 5 | SGD 5 | SGD 5 | SGD 5 |

★ Manufacture setting

Interface Settings: 2(ICT104V)

| INTERFACE | SW1 | SW2 | SW3 | SW4 |
|---------------------|-----|-----|-----|-----|
| ★ Not Used | ON | | | |
| Not Used | OFF | | | |
| Pulse Mode | | ON | OFF | |
| ★ ICT104V Interface | | OFF | ON | |
| ICT104U Interface | | OFF | OFF | |
| MDB Interface | | ON | ON | |
| ★ Reserved | | | | ON |
| | | | | OFF |

★ Manufacture setting

Interface Settings: 2(ICT104U)

| INTERFACE | SW1 | SW2 | SW3 | SW4 |
|---------------------|-----|-----|-----|-----|
| ★ Not Used | ON | | | |
| Not Used | OFF | | | |
| Pulse Mode | | ON | OFF | |
| ★ ICT104V Interface | | OFF | ON | |
| ICT104U Interface | | OFF | OFF | |
| MDB Interface | | ON | ON | |
| ★ Reserved | | | | ON |
| | | | | OFF |

★ Manufacture setting

Interface Settings: 2(MDB)

| FUNCTION | SW1 | SW2 | SW3 | SW4 |
|----------------------------------|-----|-----|-----|-----|
| ★ Scaling Factor (SF) = 100 | ON | | | |
| Decimal Point Position (DPP) = 2 | | | | |
| Scaling Factor (SF) = 1 | OFF | | | |
| Decimal Point Position (DPP) = 0 | | | | |
| Pulse Mode | | ON | OFF | |
| ICT104V Interface | | OFF | ON | |
| ICT104U Interface | | OFF | OFF | |
| ★ MDB Interface | | ON | ON | |
| ★ Reserved | | | | ON |
| | | | | OFF |

★ Manufacture setting